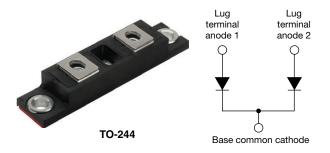


COMPLIANT

High Performance Schottky Rectifier, 200 A



PRIMARY CHARACTERISTICS				
I _{F(AV)}	200 A			
V_{R}	45 V			
Package	TO-244			
Circuit configuration	Two diodes common cathode			

FEATURES

- 175 °C T_J operation
- · Center tap module
- · Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-201CNQ045PbF center tap Schottky rectifier module has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	200	А		
V _{RRM}		45	V		
I _{FSM}	t _p = 5 μs sine	16 000	А		
V _F	100 A _{pk} , T _J = 125 °C (per leg)	0.58	V		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-201CNQ045PbF	UNITS
Maximum DC reverse voltage	V_{R}	45	V
Maximum working peak reverse voltage	V_{RWM}	45	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per device forward current		50 % duty cycle at T _C = 146 °C, rectangular waveform		200	А
See fig. 5 per leg	J I _{F(AV)}			50 % duty cycle at 1 _C = 140 °C, rectangular wavelonn	100
Maximum peak one cycle non-repetitive surge current per leg		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	16 000	А
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	2000	
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 17 A, L = 1 mH		145	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		20	Α





ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	100 A	T _{.1} = 25 °C	0.67	V
		200 A	1] = 25 0	0.81	
		100 A	T _{.1} = 125 °C	0.58	
		200 A	1J = 125 C	0.71	
Maximum reverse leakage current per leg See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	10	- mA
		T _J = 125 °C	VR = nateu VR	90	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		5200	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		7.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}	- 55	-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}	-	-	0.38	°C/W	
per module	PthJC	-	-	0.19		
Thermal resistance, case to heatsink	R _{thCS}	-	0.10	-		
Weisla		-	68	-	g	
Weight			2.4		OZ.	
Mounting torque		35.4 (4)	-	53.1 (6)		
Mounting torque center hole		30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)	
Terminal torque		30 (3.4)	-	44.2 (5)		
Vertical pull		-	-	80	llef in	
2" lever pull		-	-	35	- lbf · in	



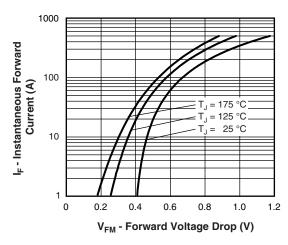


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

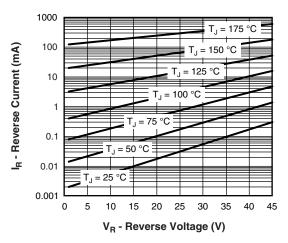


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

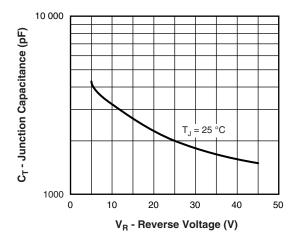


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

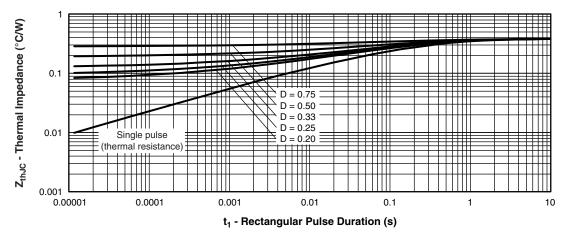


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)



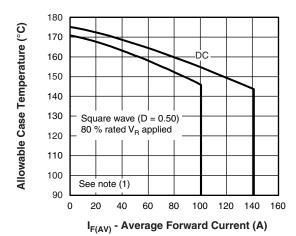


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

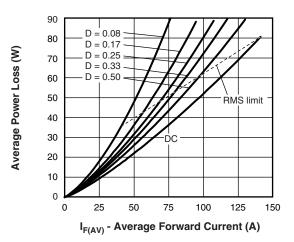


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

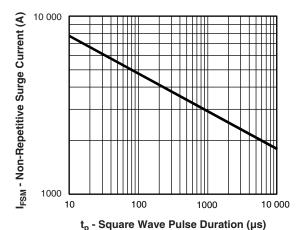


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

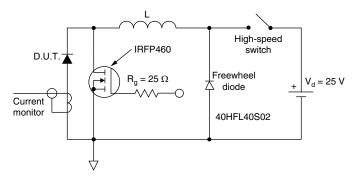


Fig. 8 - Unclamped Inductive Test Circuit

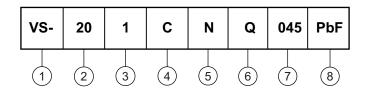
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

Average current rating (x 10)

3 - Product silicon identification

4 - C = circuit configuration

5 - N = not isolated

6 - Q = Schottky rectifier diode

7 - Voltage rating (045 = 45 V)

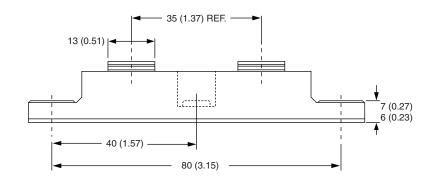
Lead (Pb)-free

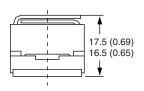
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95021		

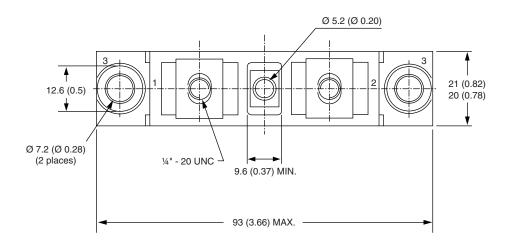


TO-244

DIMENSIONS in millimeters (inches)









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